

R104-88 User Guide

A) Relay Output Control

The 8 relays are accessed through I/O memory writes. The relays are grouped in sets of four, and the group I/O memory address is an offset from the base decode address. Relays are grouped as follows:

Group 1: Outputs DO1 to DO4 I/O address = Base Address
Group 2: Outputs DO5 to DO8 I/O address = Base Address + 1

The relays are bit mapped to the lower four data lines in each group as follows:

Relay	SD3	SD2	SD1	SD0
Group 1	Relay4	Relay3	Relay2	Relay1
Group 2	Relay8	Relay7	Relay6	Relay5

B) Digital Input Reading

The 8 digital inputs are accessed through I/O memory reads. The inputs are grouped in sets of four, and the group I/O memory address is an offset from the base decode address. Inputs are grouped as follows:

Group 1: Inputs DO1 to DO4 I/O address = Base Address + 2
Group 2: Inputs DO5 to DO8 I/O address = Base Address + 3

The inputs are bit mapped to the lower four data lines in each group as follows:

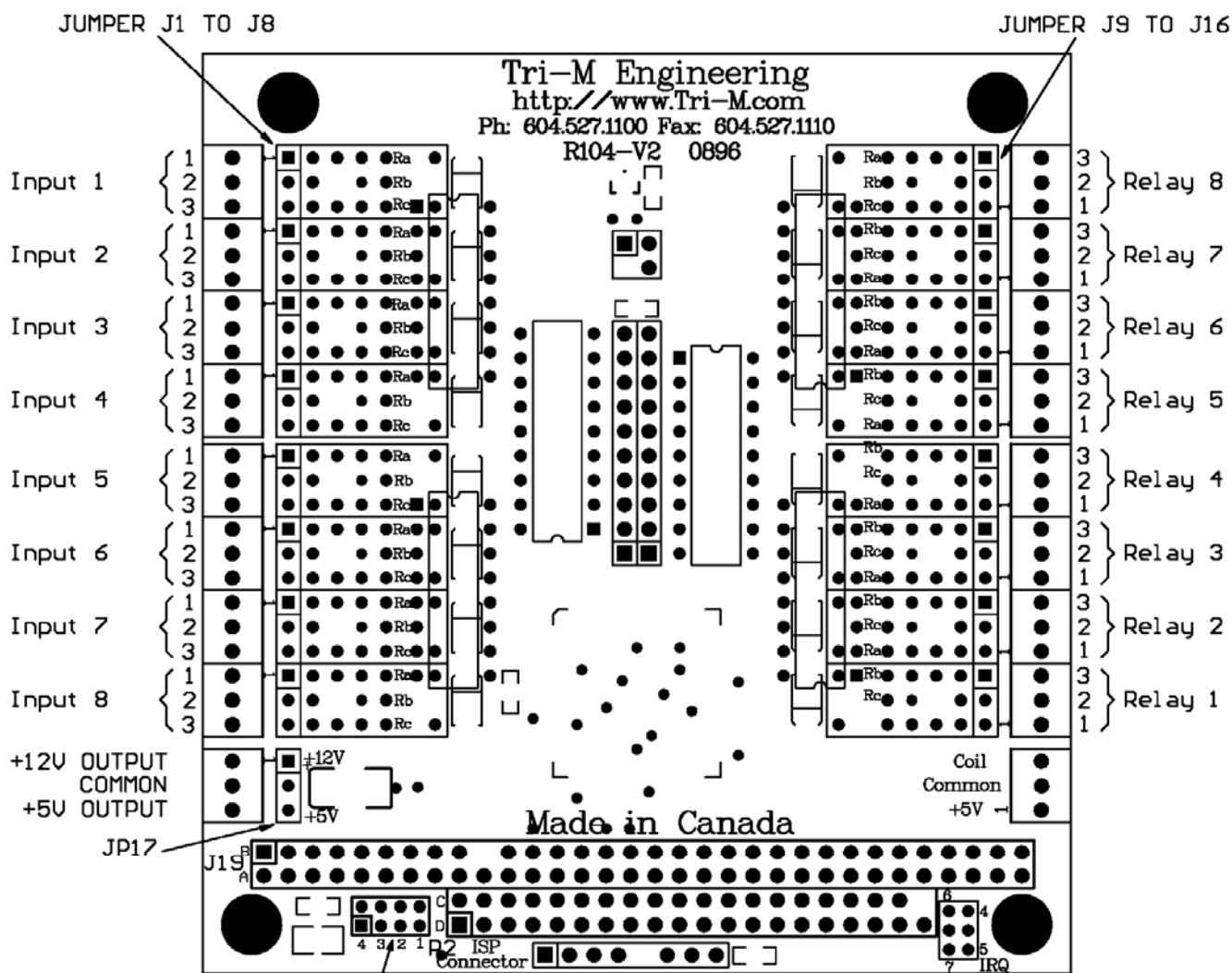
Digital Input	SD3	SD2	SD1	SD0
Group 1	Input4	Input3	Input2	Input1
Group 2	Input8	Input7	Input6	Input5

C) Base Address Setting

There are four decode base addresses, which are jumper selectable from the address select block J18.

Base Address	J18-1	J18-2
240H	Jumper Not Installed	Jumper Not Installed
260H	Jumper Installed	Jumper Not Installed
280H	Jumper Not Installed	Jumper Installed
300H	Jumper Installed	Jumper Installed

R104 Digital I/O Relay Board



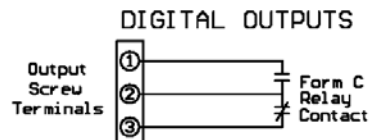
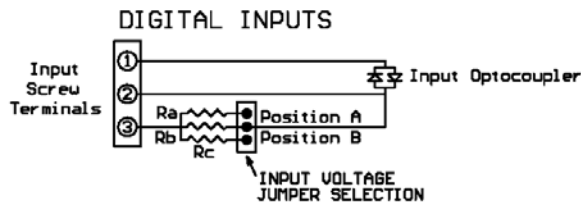
Relay Board I/O Address Selection

JP18	1	2
ADDR 240H	OFF	OFF
ADDR 260H	ON	OFF
ADDR 280H	OFF	ON
ADDR 300H	ON	ON

Digital Input Voltage Selection

Jumper	A	B
4-5V Input	ON	OFF
10-15V Input	OFF	ON
24-28V Input	OFF	OFF
* Other	OFF	OFF

- * Use external limit resistor in series with terminal 1 & 2
- ** Digital Inputs are not polarity Sensitive



Input signal must be connected to terminals 1 and 3